



Docket No.: 394-1597

3/Priority Papers  
7-16-94  
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Seiji SAWADA et al.

Serial No. 08/246,582

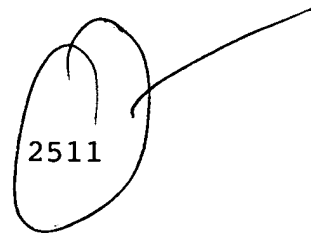
Filed: May 19, 1994

For: TEST CIRCUIT IN CLOCK SYNCHRONOUS SEMICONDUCTOR MEMORY  
DEVICE

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Group Art Unit: 2511

Examiner:



TRANSMITTAL OF CERTIFIED PRIORITY DOCUMENT

Honorable Commissioner of  
Patents and Trademarks  
Washington, D.C. 20231

Sir:

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At the time of filing the above-identified application, a  
claim was made to the priority date(s) of the corresponding  
Japanese Patent Application Serial No. 5-122439  
filed May 25, 1993

We now submit the priority document(s) in support of this  
Claim of Priority. Acknowledgement is respectfully requested.

Respectfully submitted,

LOWE, PRICE, LEBLANC & BECKER

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JUN 24 1994  
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Enclosure  
Date: June 24, 1994